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FIG. 1A
PRIOR ART

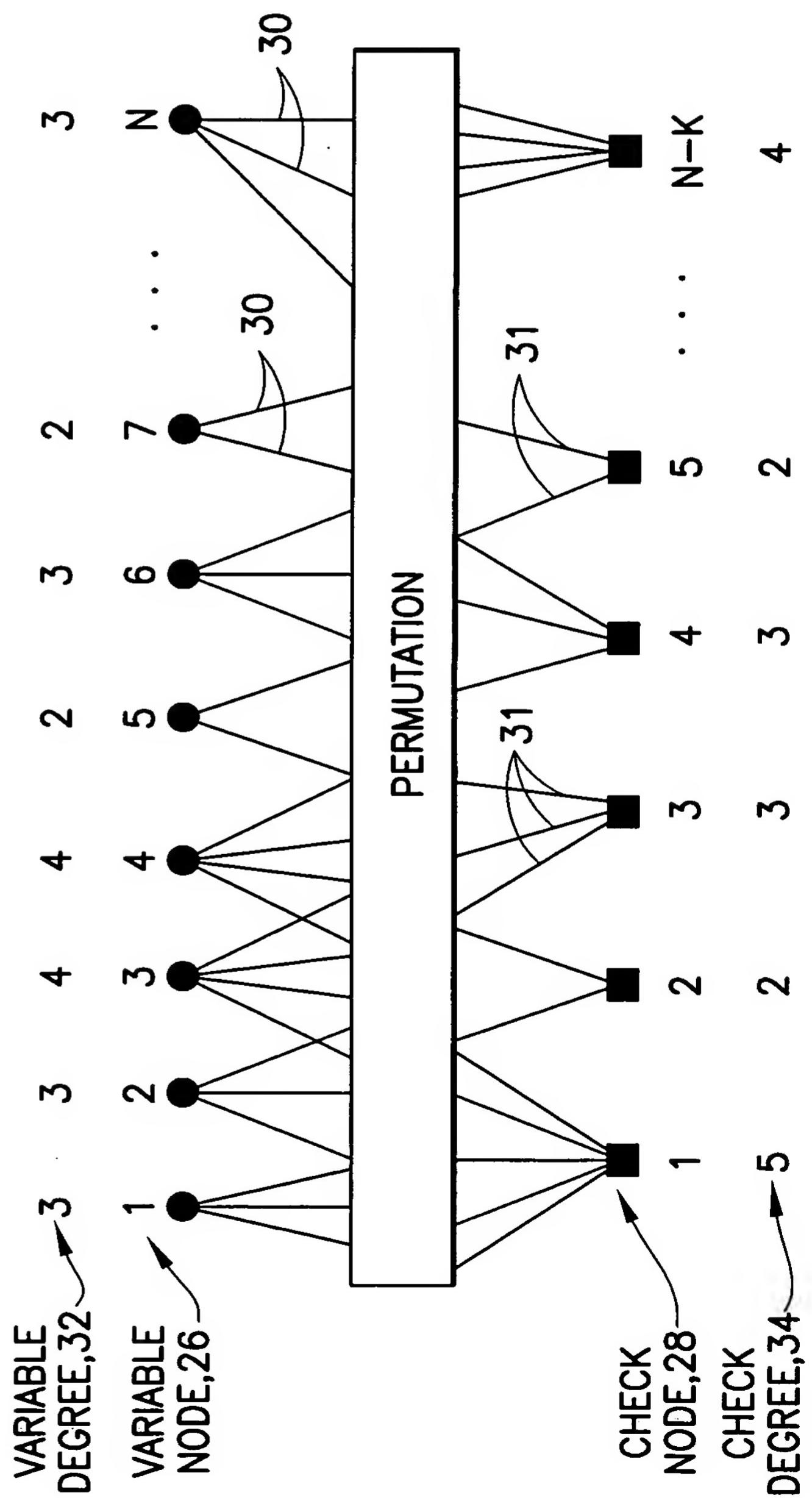
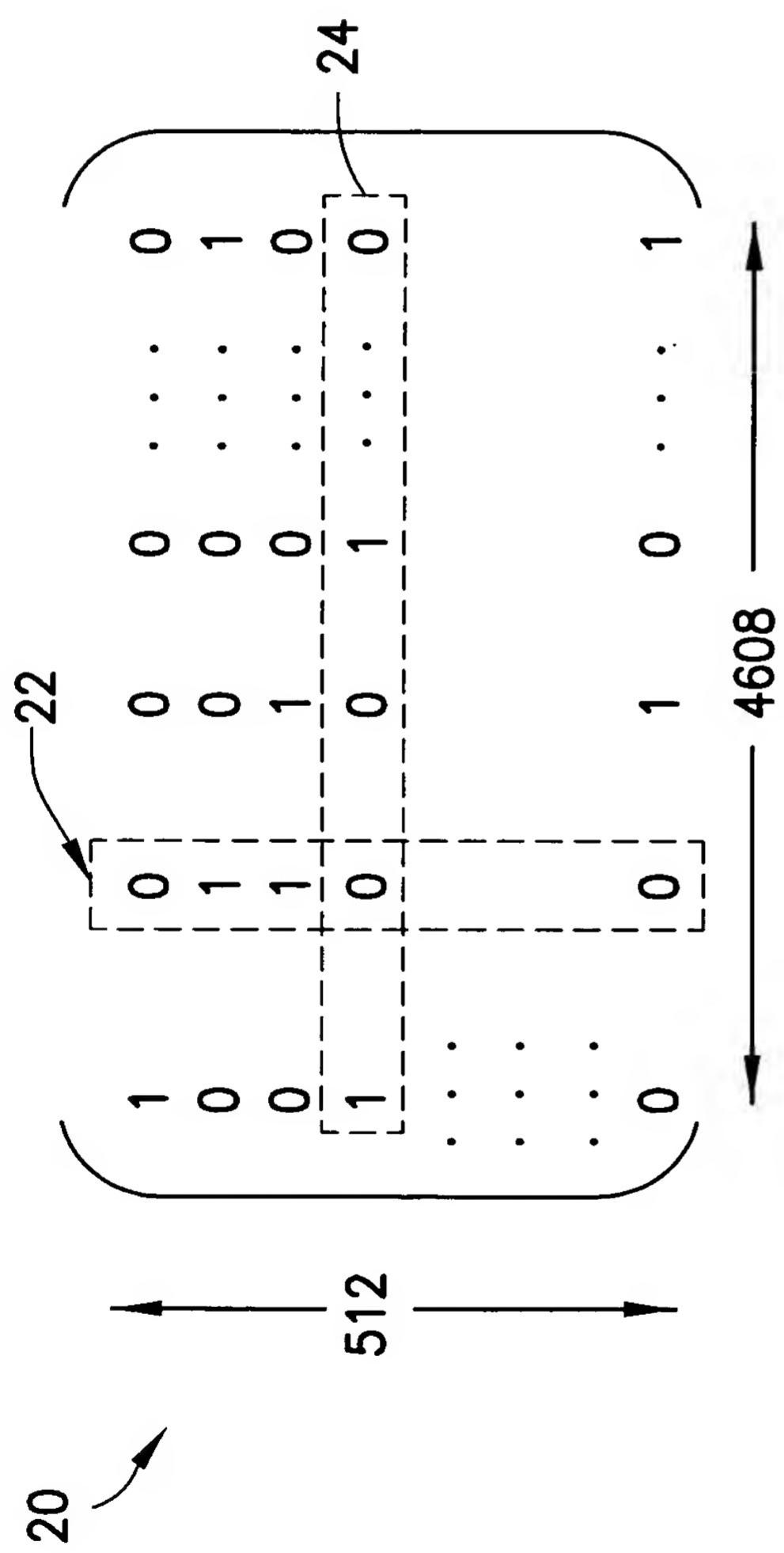
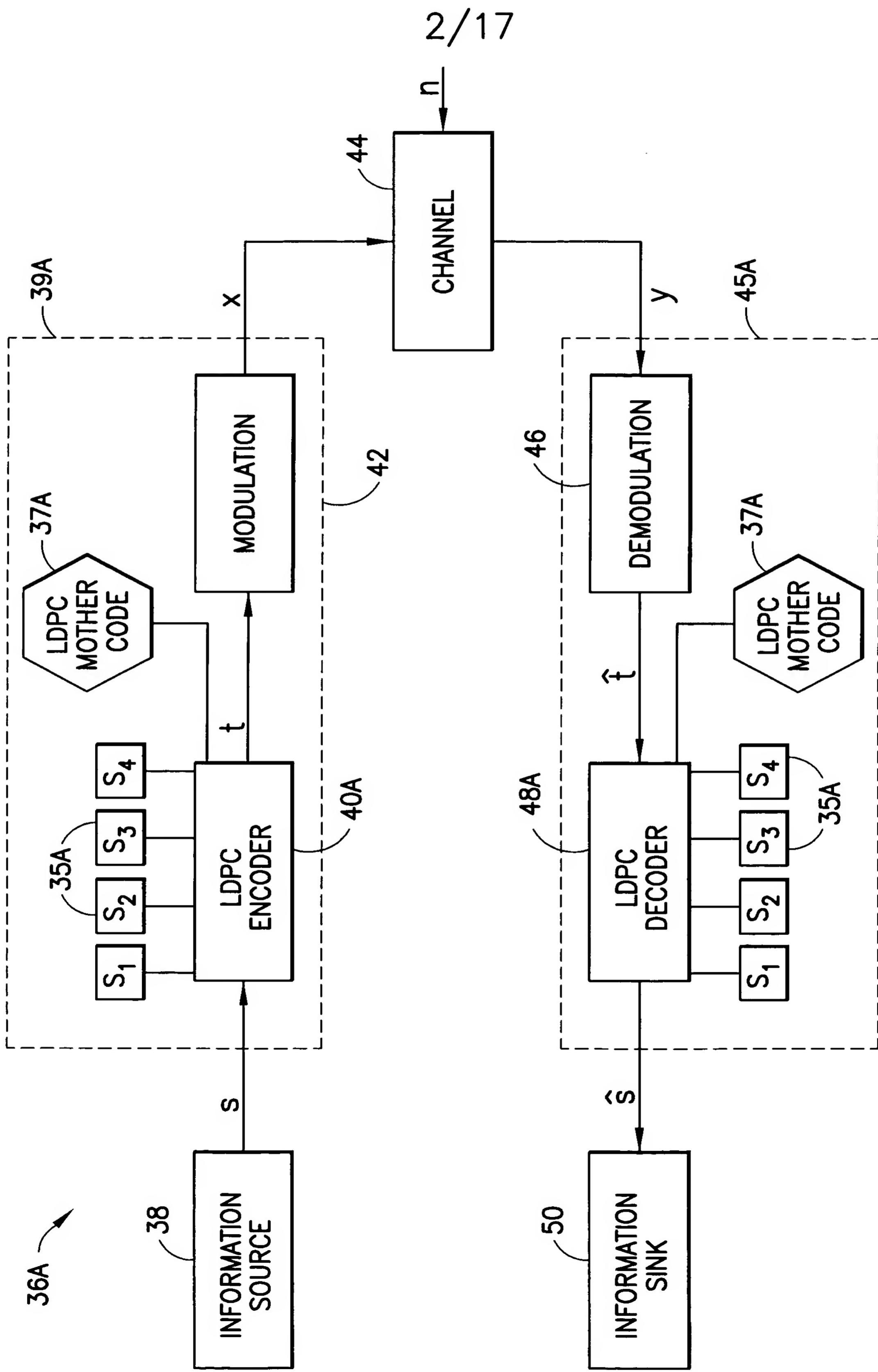


FIG. 1B
PRIOR ART

FIG. 2A
PRIOR ART



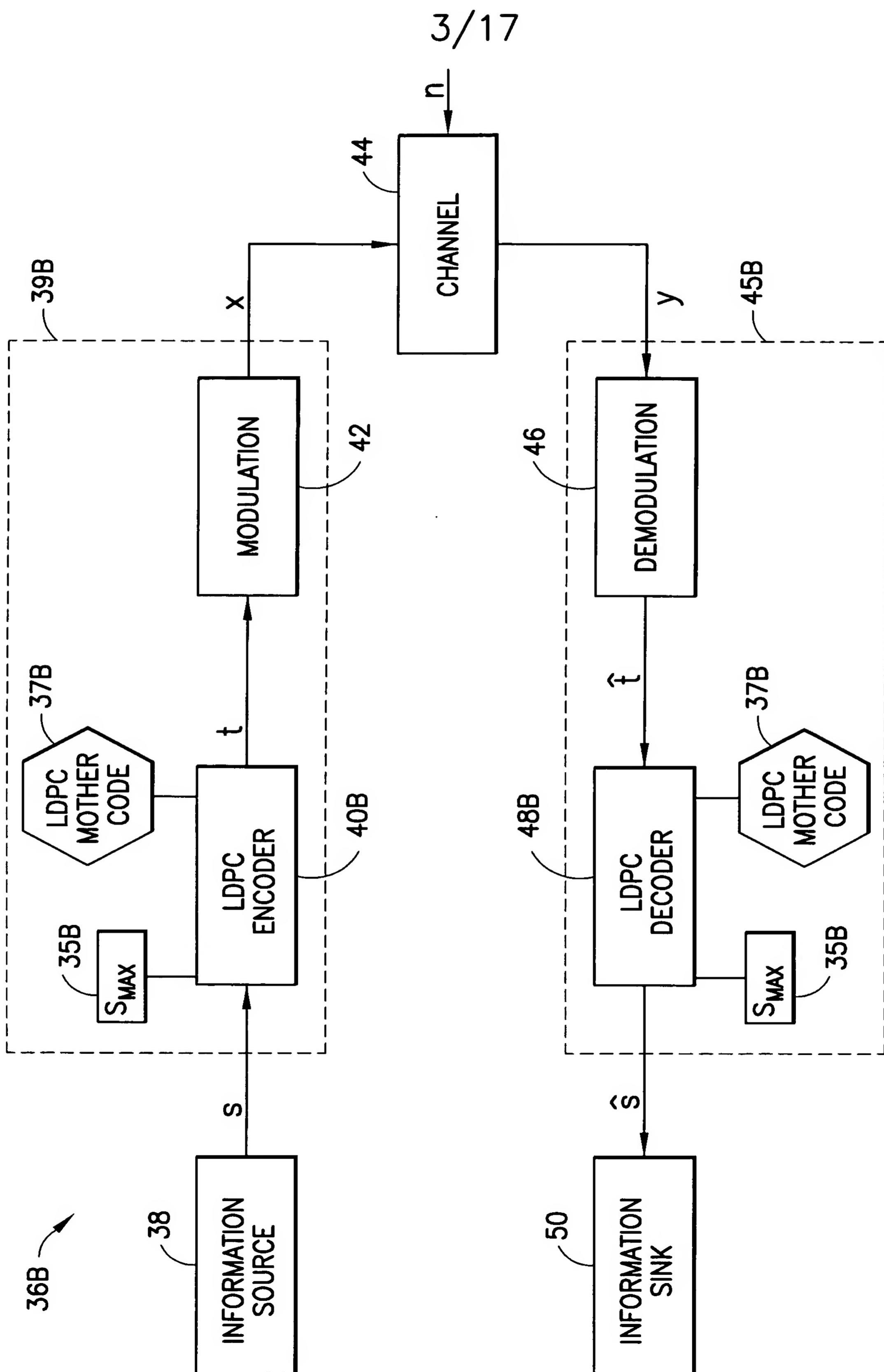


FIG. 2B

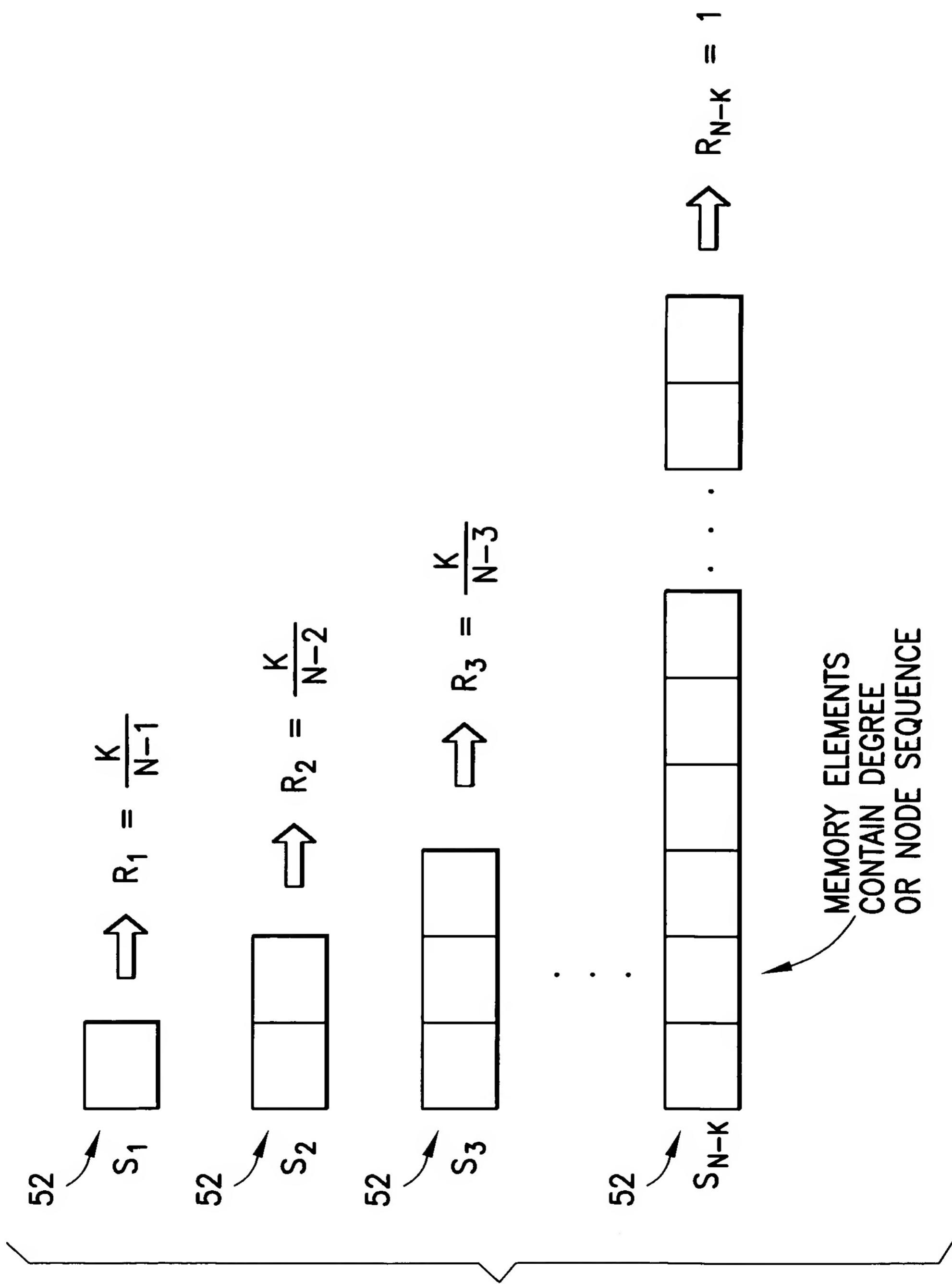


FIG.3
PRIOR ART

$S_1 \subseteq S_2 \subseteq \dots \subseteq S_{N-K-1} \subseteq S_{N-K}$

54, MEMORY ELEMENTS
CONTAIN DEGREE
OR NODE SEQUENCE

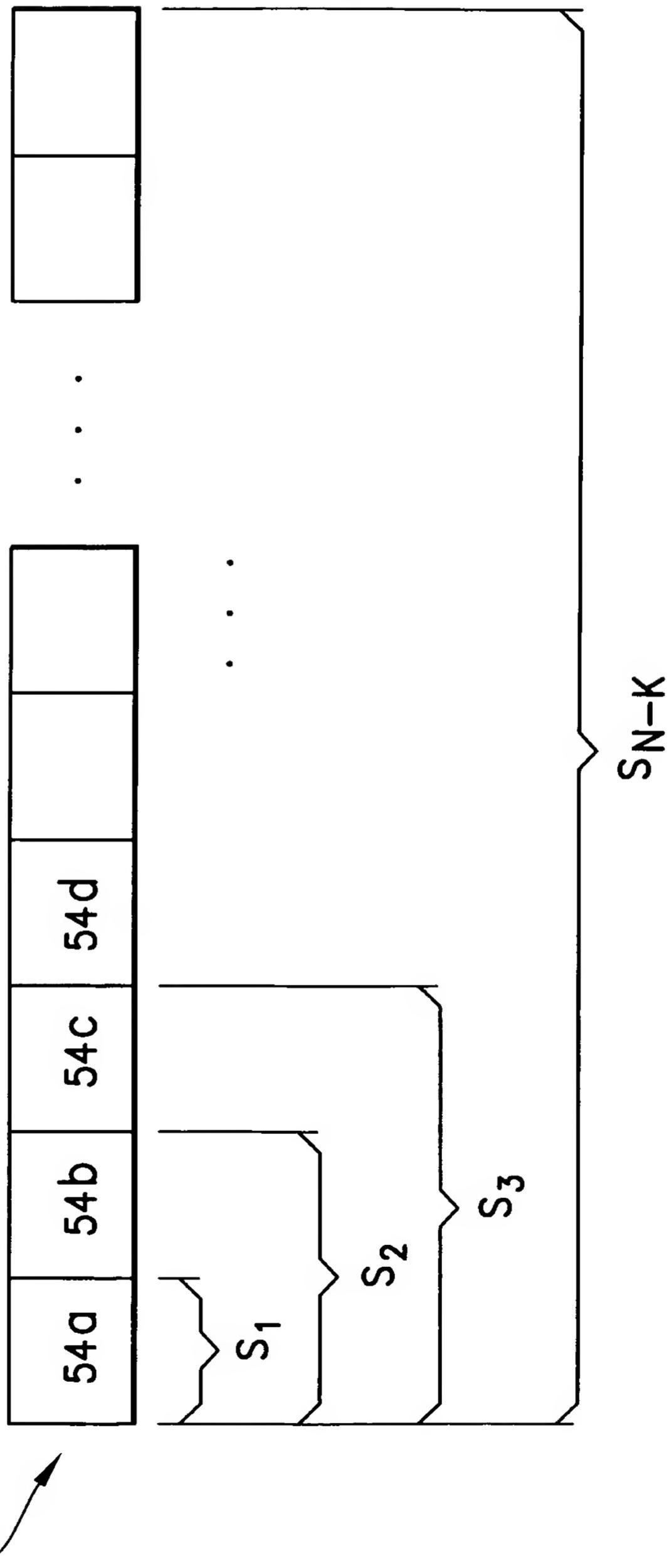


FIG.4

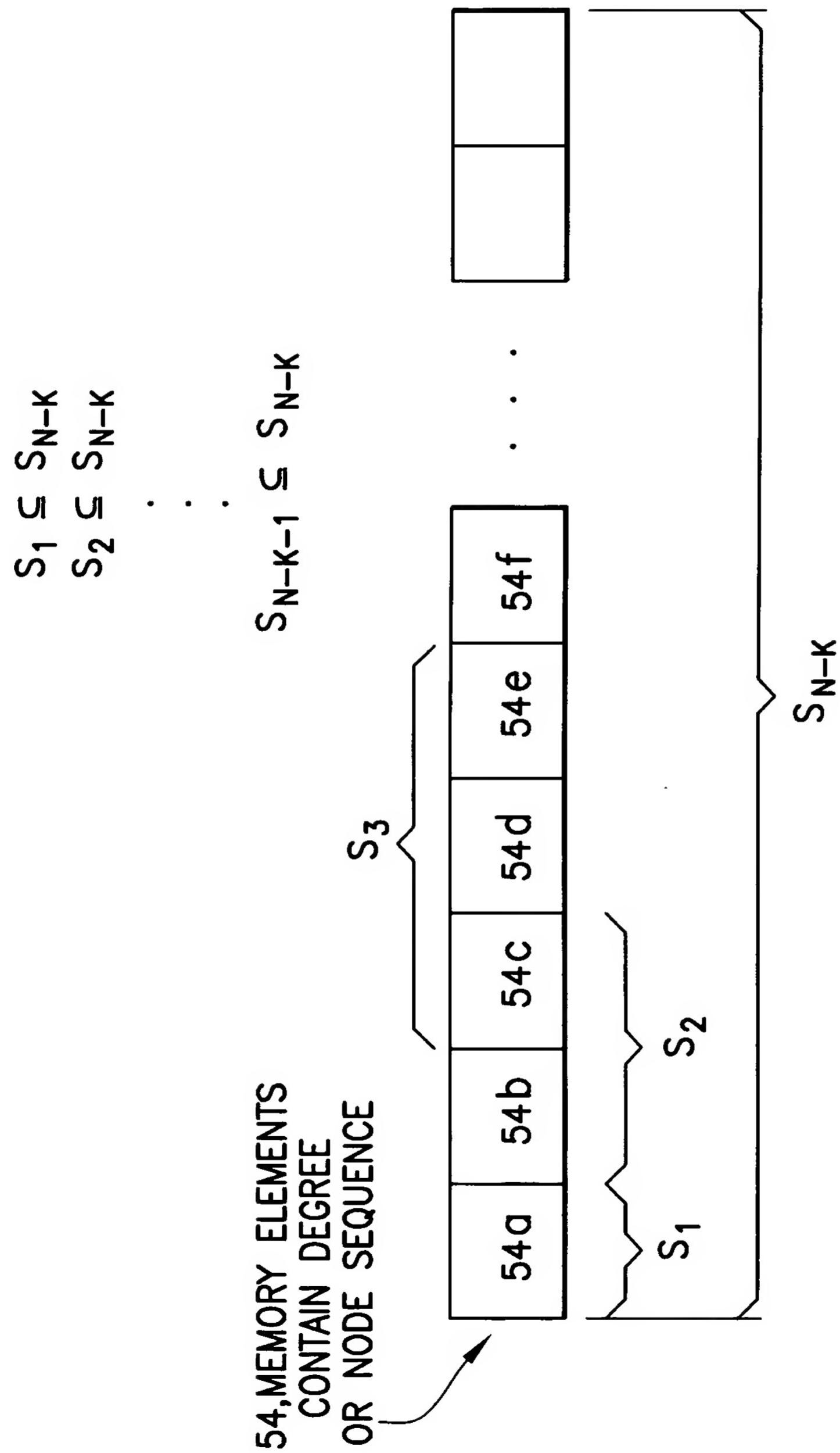


FIG.5

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FIG. 6

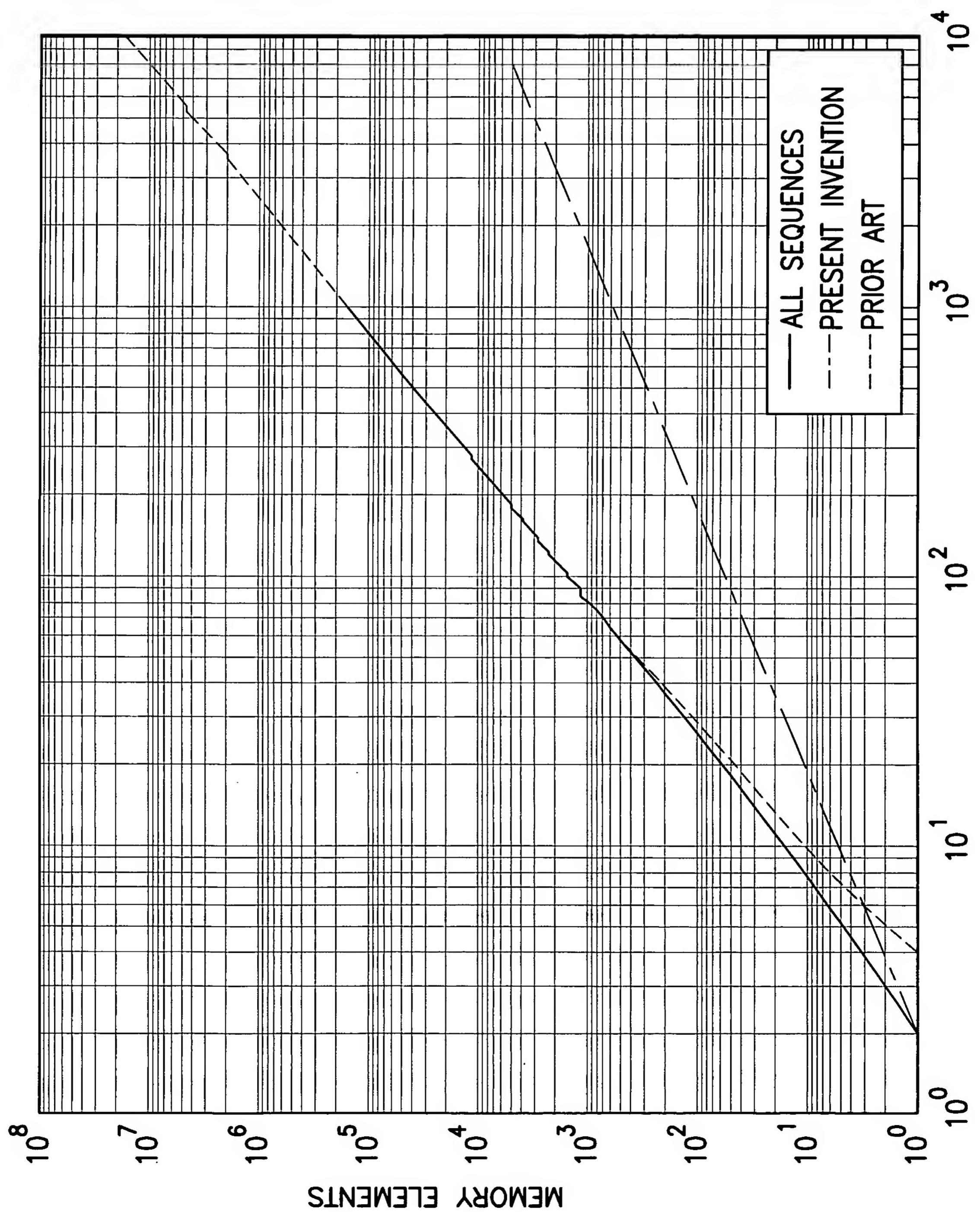
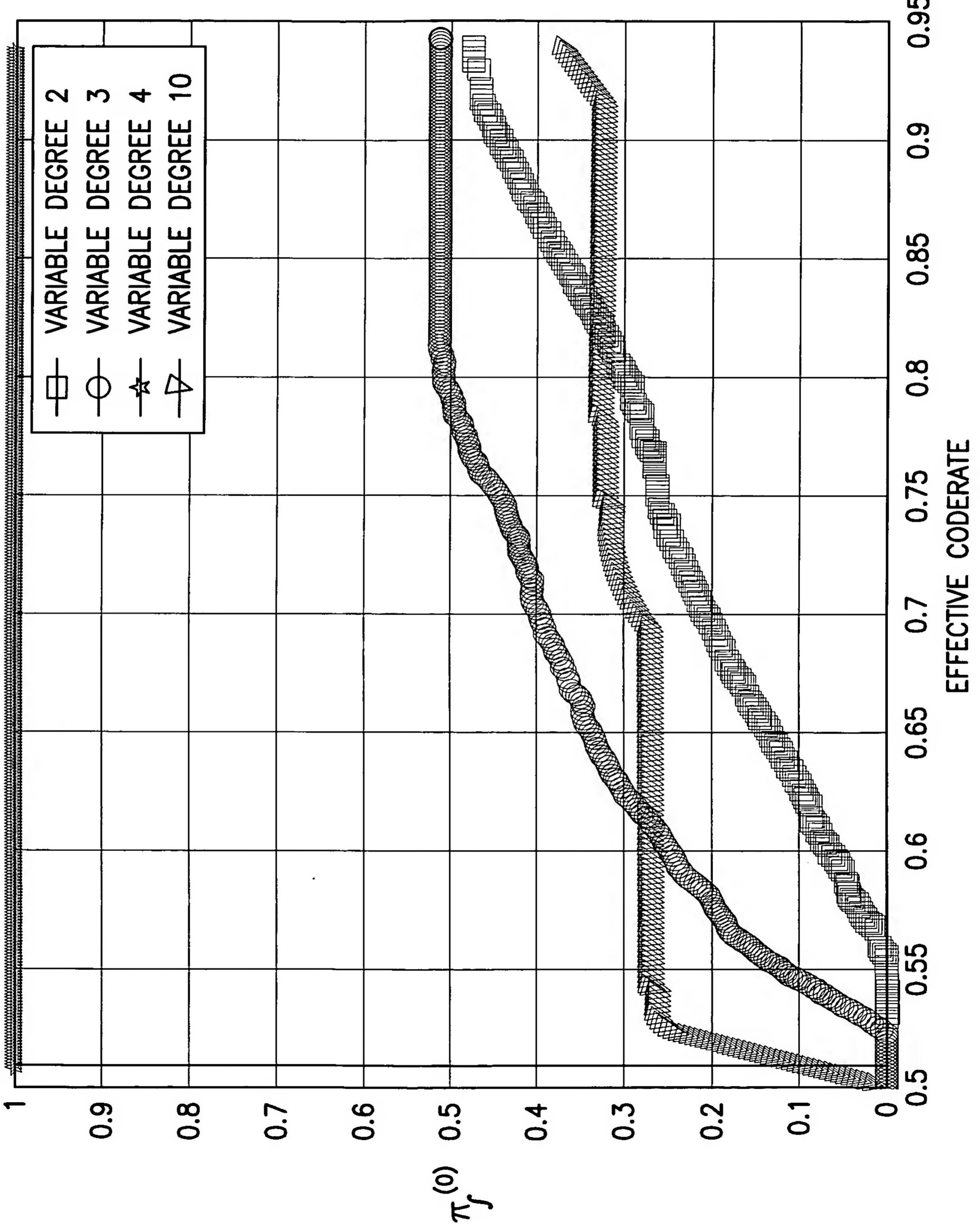


FIG. 7

LDPC PUNCTURED N1082 M541CODE12.mat



LDPC PUNCTURED N3255 M1627CODE11.mat

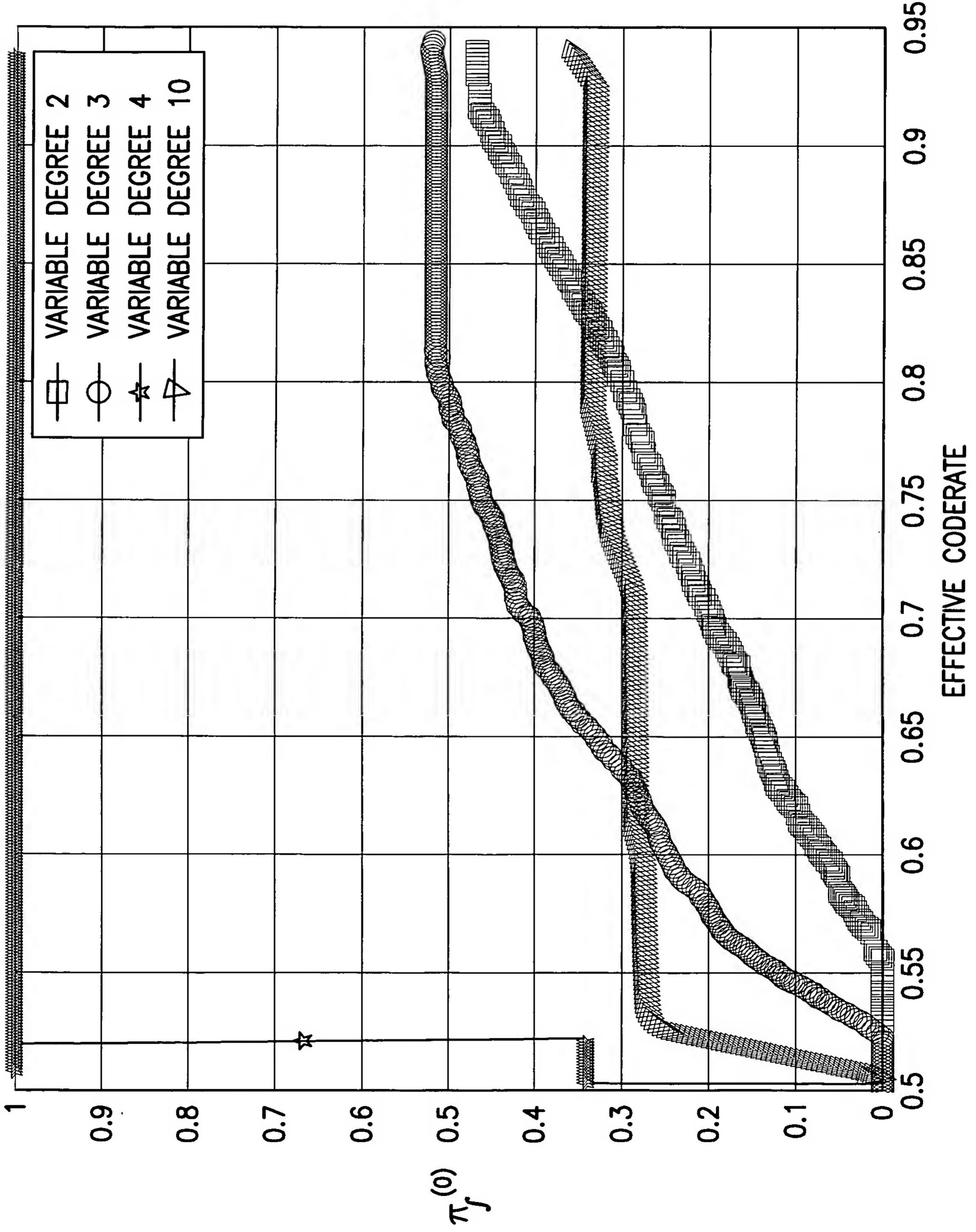
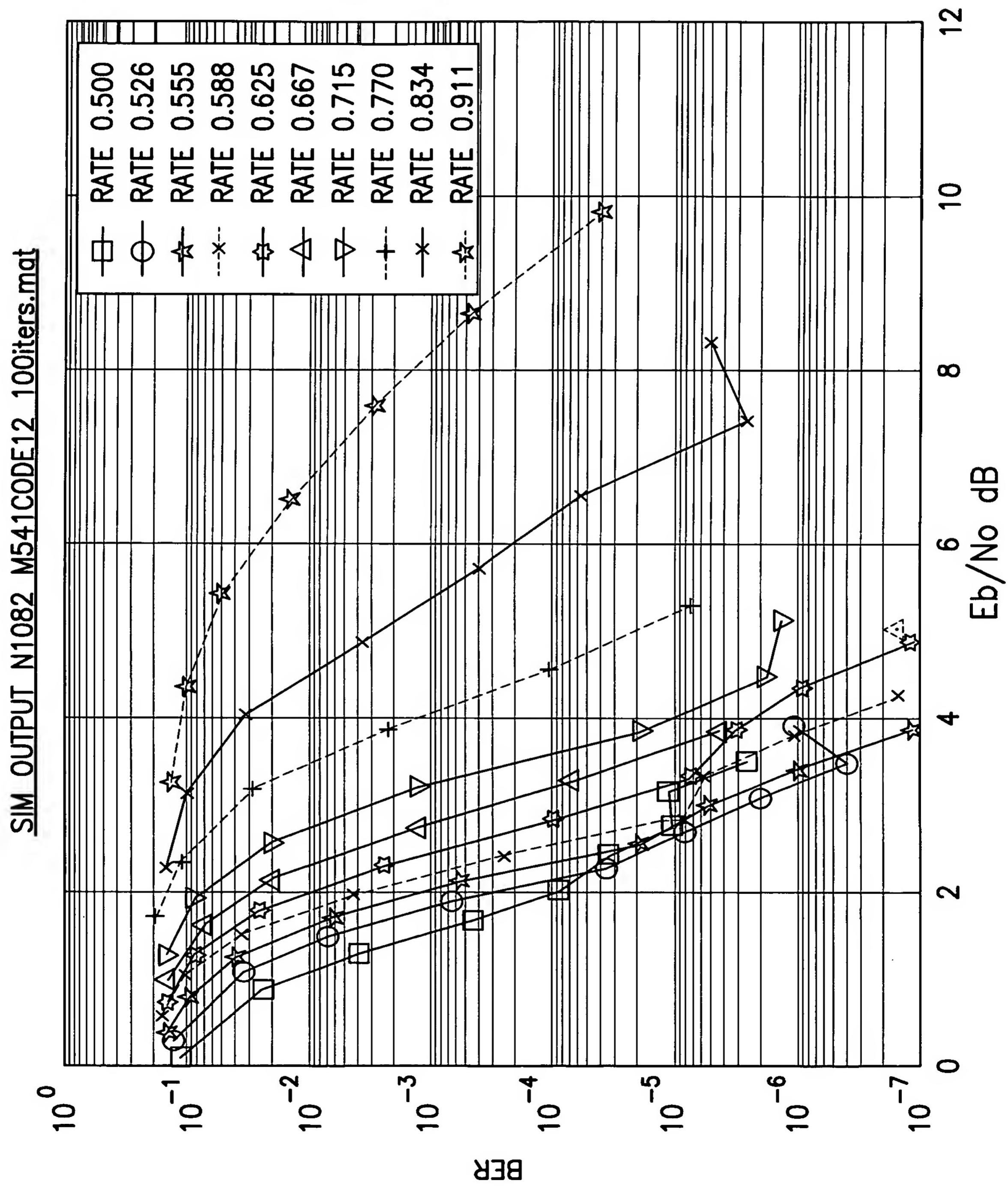
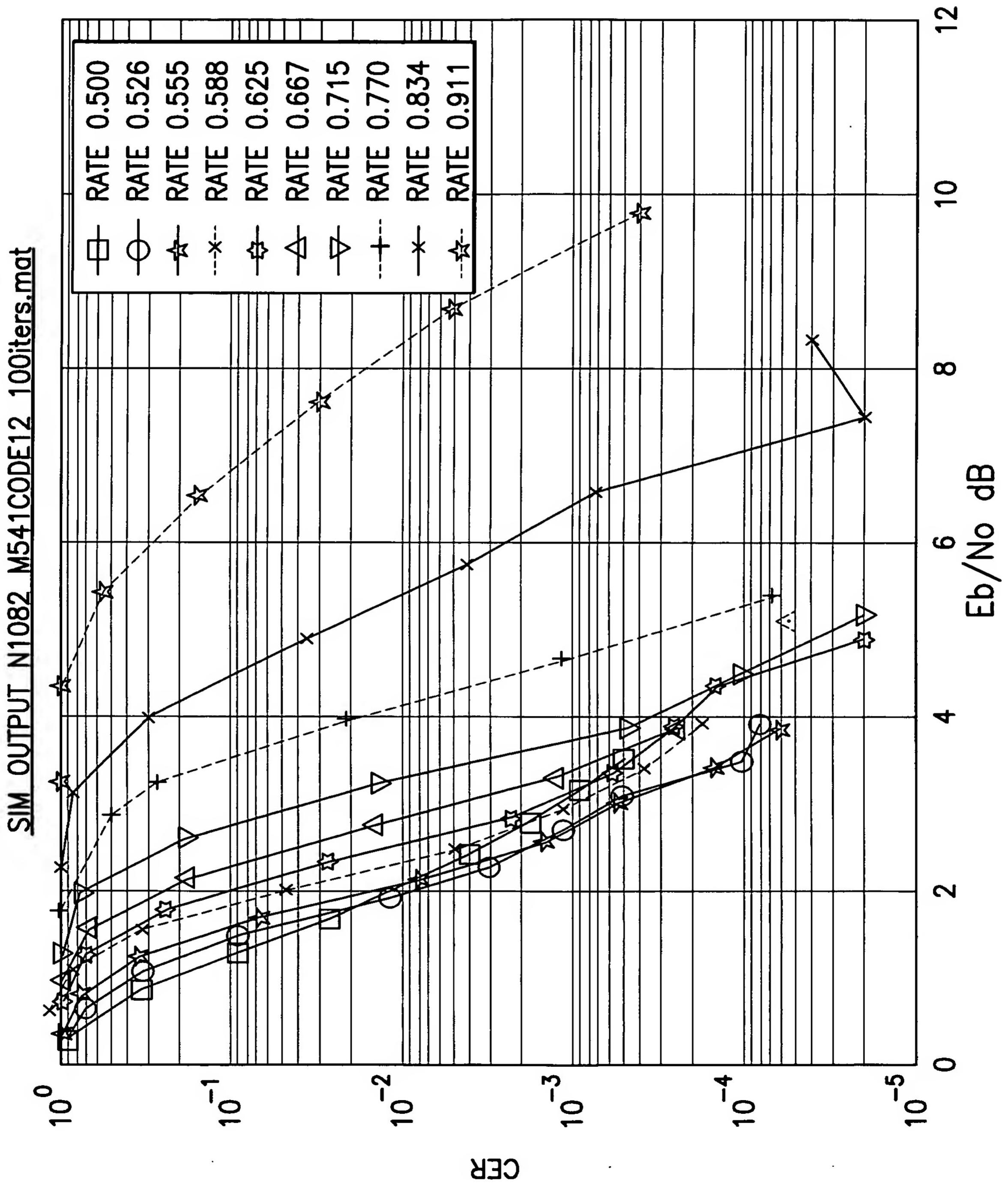


FIG.9



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FIG. 10



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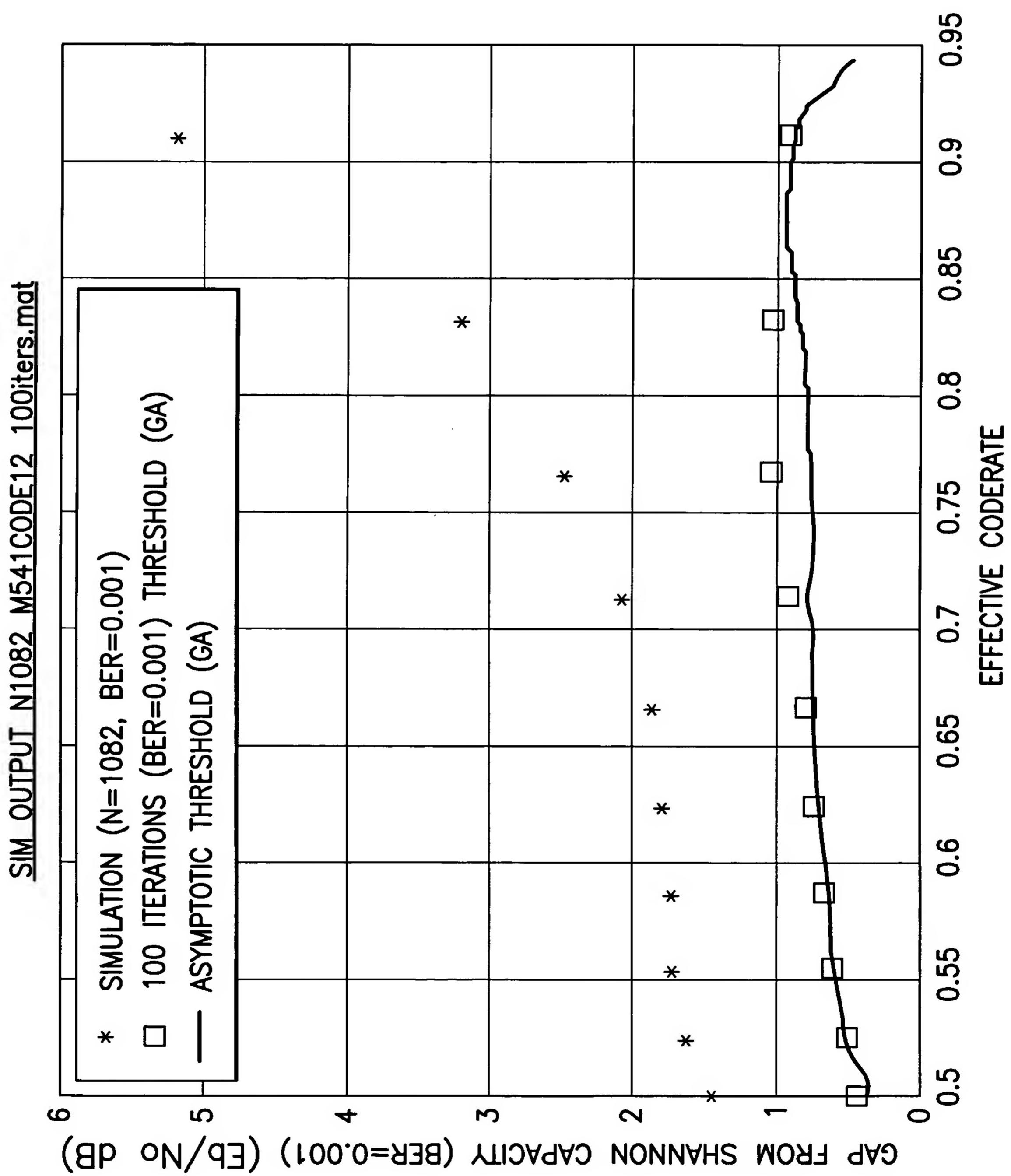


FIG. 11

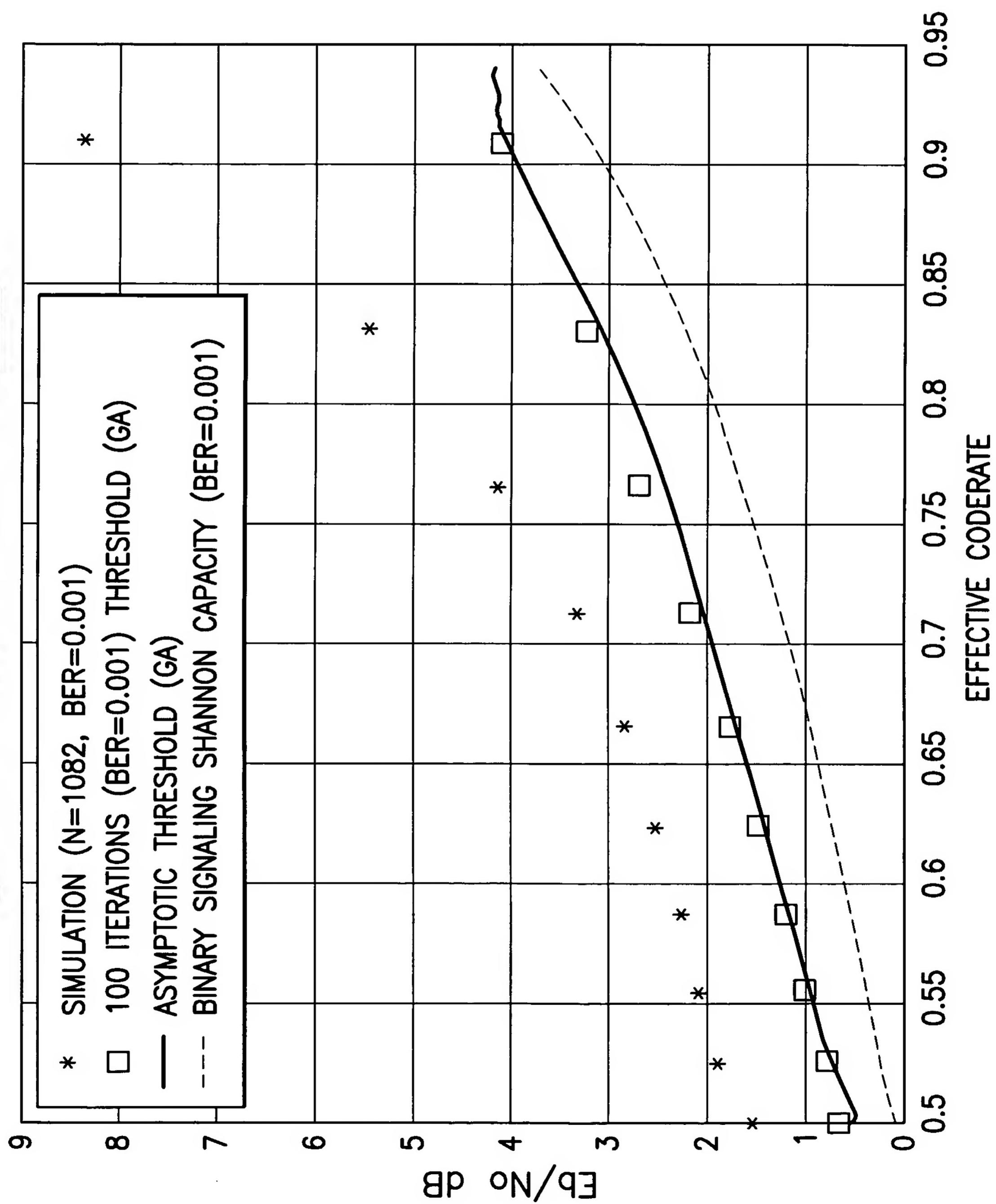
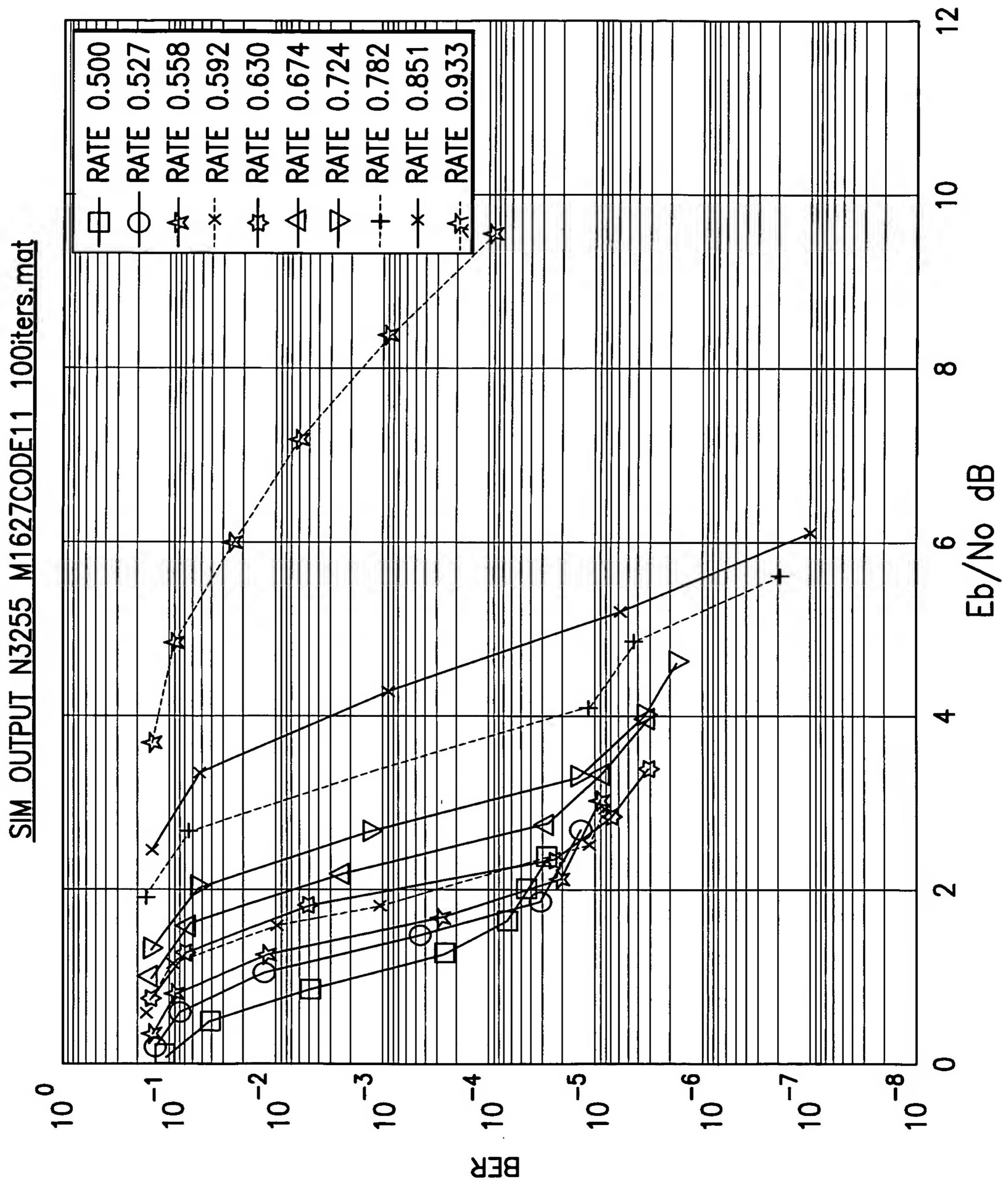
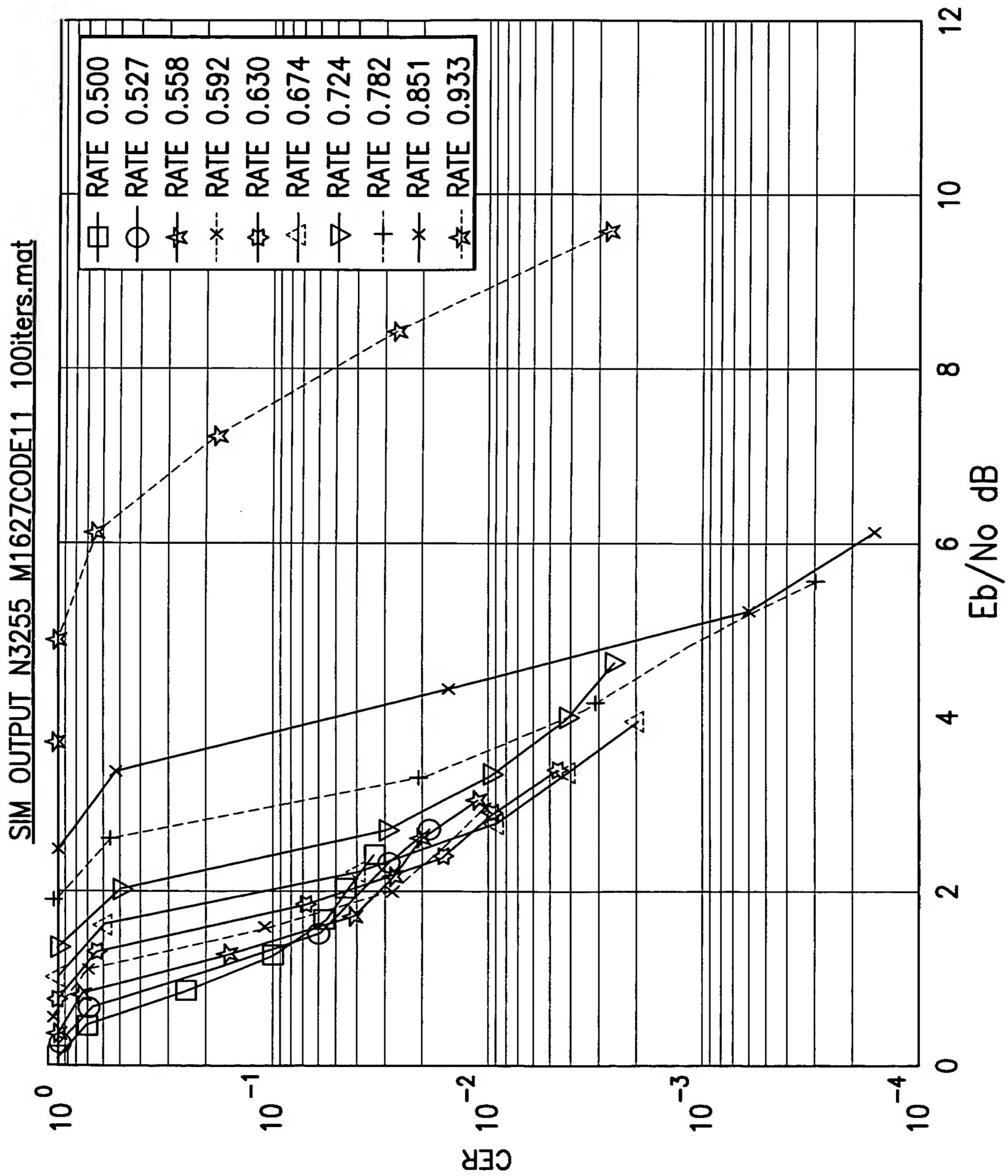
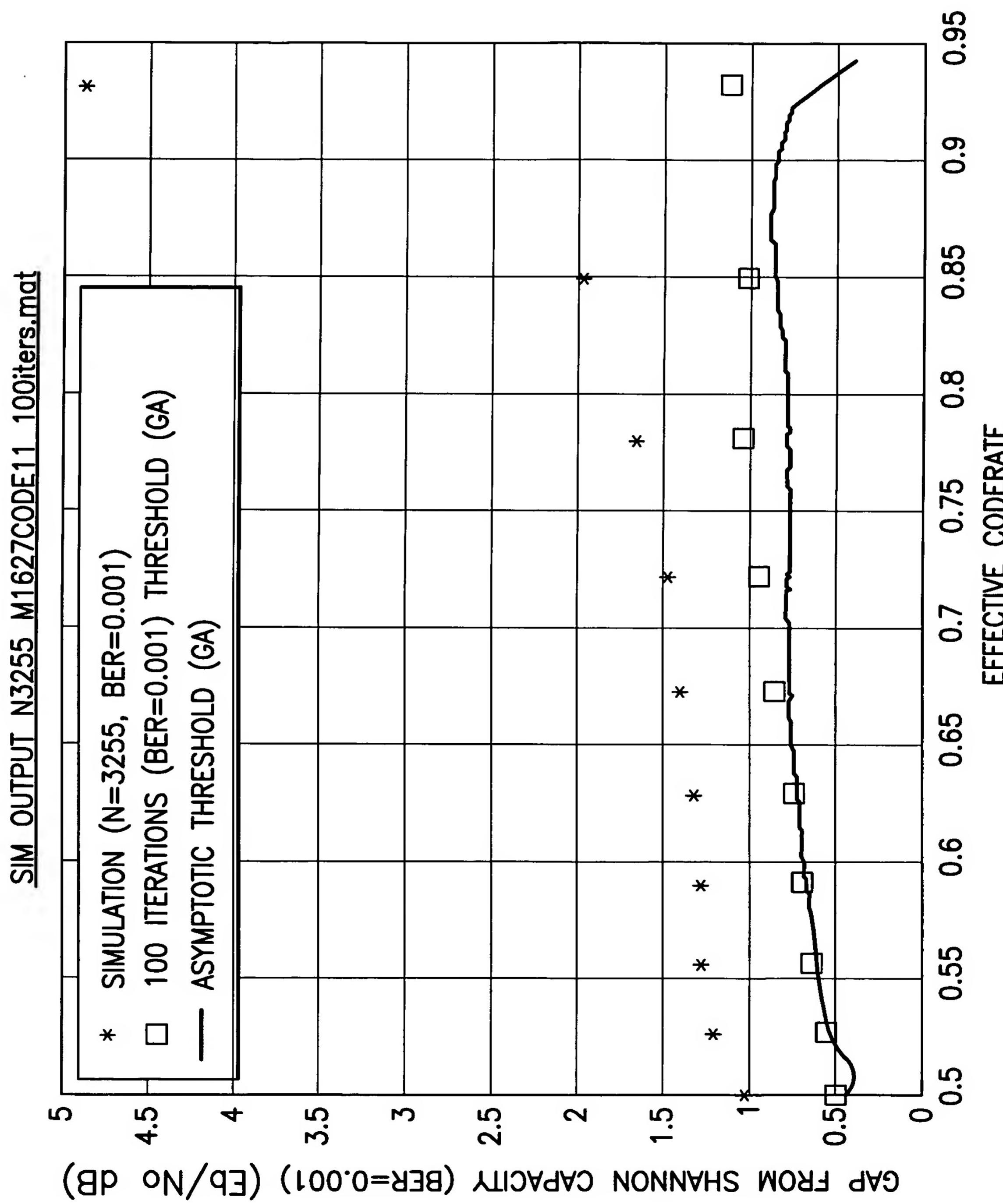
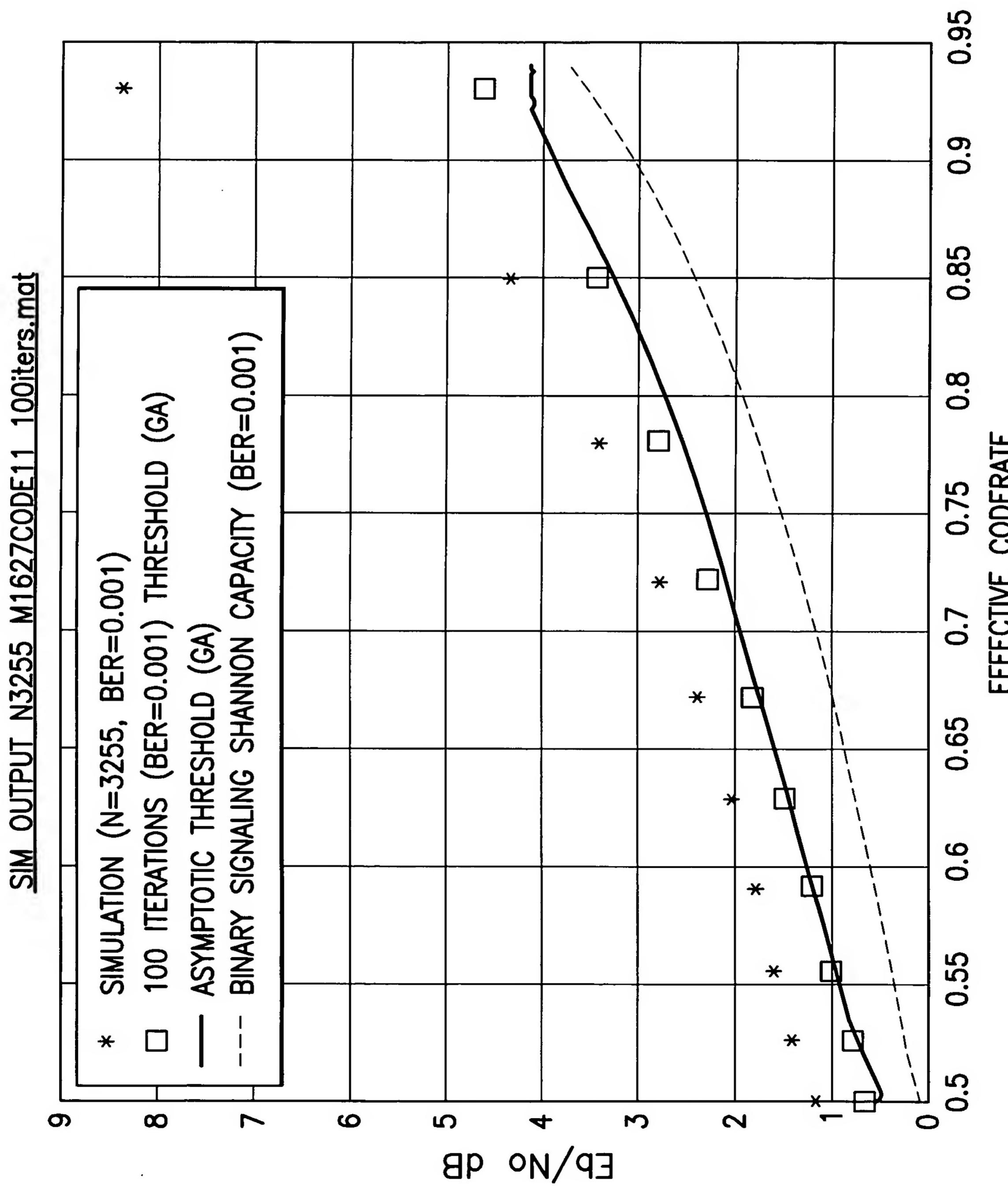
SIM_OUTPUT_N1082_M541CODE12_100iters.mat**FIG. 12**

FIG. 13





**FIG. 15**

**FIG. 16**